Electronic Acknowledgement Receipt

EFS ID:	2512884
Application Number:	10631246
International Application Number:	
Confirmation Number:	1610
Title of Invention:	Processor that accommodates multiple instruction sets and multiple decode modes
First Named Inventor/Applicant Name:	Gerard Chauvel
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Attorney Docket Number:	TI-35434
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Fee Worksheet (PTO-06)	Fee_Trl.pdf	81914 42fdd83e5b6e908ecc47e1c77ce45857 9168c419	no	1

Warnings:	
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2		ROA_06-04-07.pdf	113483 9d660eba00b89c57411fb090a60430f148962ad3	yes	14

Multipart Description/PDF files in .zip description		
Document Description	**Start**	**End**
Amendment - After Non-Final Rejection	1	1
Claims	2	8
Applicant Arguments/Remarks Made in an Amendment	9	14

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3	Information Disclosure Statement (IDS) Filed	IDS.pdf	717271 6b2d3c1ded4e87b8b617a11cc01c0a8983ec2152	no	4

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4	NPL Documents	NPL_1.pdf	797537 f33aa3448707f96e839c05952efcc87b5bccdec1	no	27

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5	NPL Documents	NPL_2.pdf	715590 5bdd3a64b7e82f9ba93b70540300c3474cd15df2	no	24

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6	Extension of Time	3_mo_Extn.pdf	64106 8b05fa7272b53ad961ab013feea4a4acd30d46c9	no	1

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